

Set Name Query
side by side

Hit Count Set Name
result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L11</u>	110 and ((binomial or dual\$4) near8 (bank or module or cell or memory))	54	<u>L11</u>
<u>L10</u>	L9 and ((711/\$ or 709/\$ or 370/\$ or 710/\$ or 365/\$).ccls.)	154	<u>L10</u>
<u>L9</u>	L8 and ((memory or memories or bank or module or location or cell) with parallel)	250	<u>L9</u>
<u>L8</u>	L7 and ((random\$2 or arbitrar\$3) same (speed or latency or bandwidth))	606	<u>L8</u>
<u>L7</u>	L6 and ((control\$4 or arbit\$6 or schedul\$4) near8 (memory or memories or bank or module or location or cell or queu\$3 or buffer\$3))	2872	<u>L7</u>
<u>L6</u>	L1 and ((choos\$3 or chos\$4 or pick\$4 or writ\$3 or stor\$3 or select\$3 or priorit\$6 or order\$3 or arrang\$3) near6 (random\$2 or arbitrar\$3 or different) near8 (module or bank or location or cell or memories))	3391	<u>L6</u>
<u>L5</u>	L4 and ((memory or memories or bank or module or location or cell) with parallel)	55	<u>L5</u>
<u>L4</u>	L3 and ((random\$2 or arbitrar\$3) same (speed or latency or bandwidth))	117	<u>L4</u>
<u>L3</u>	L2 and ((control\$4 or arbit\$6 or schedul\$4) near8 (memory or memories or bank or module or location or cell or queu\$3 or buffer\$3))	333	<u>L3</u>
<u>L2</u>	L1 and ((random\$2 or arbitrar\$3) near4 (choos\$3 or chos\$4 or pick\$4 or writ\$3 or stor\$3 or select\$3 or priorit\$6 or order\$3 or arrang\$3) near8 (module or bank or location or cell))	362	<u>L2</u>
<u>L1</u>	packet near6 (stor\$4 or writ\$4 or buffer\$3 or queu\$3 or receiv\$4)	21683	<u>L1</u>

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<u>L16</u>	L15 and l11	0	<u>L16</u>
<u>L15</u>	L13 and (random\$2 near4 (writ\$3 or stor\$3 or select\$3 or priorit\$6 or order\$4 or arrang\$4) near8 module)	44	<u>L15</u>
<u>L14</u>	L13 and l11	17	<u>L14</u>
<u>L13</u>	packet near6 (stor\$4 or writ\$4 or buffer\$3 or queue\$3)	13528	<u>L13</u>
<u>L12</u>	L11 and l10	16	<u>L12</u>
<u>L11</u>	module near4 (coupl\$4 or arrang\$6 or config\$4 or organiz\$4 or dispos\$4) near6 parallel	1422	<u>L11</u>
<u>L10</u>	packet near6 (stor\$4 or writ\$4 or buffer\$3 or queue\$3)	11971	<u>L10</u>
<u>L9</u>	L8 and ((buffer\$3 or FIFO or queue\$4) near8 module)	12	<u>L9</u>
<u>L8</u>	((control\$4 or arbit\$6 or schedul\$4) near4 random\$2 near4 (writ\$3 or stor\$3 or select\$3 or priorit\$6 or order\$4 or arrang\$4) near8 module)	48	<u>L8</u>
<u>L7</u>	((control\$4 or arbit\$6 or schedul\$4) near4 random\$2 near8 (writ\$3 or stor\$3 or select\$3 or priorit\$6 or order\$4 or arrang\$4) near6 module)	69	<u>L7</u>
<u>L6</u>	L5 and ((queue\$4 or buffer\$3) near8 module)	8	<u>L6</u>
<u>L5</u>	L4 and ((plurality or several or multiple) adj3 module)	33	<u>L5</u>
<u>L4</u>	L1 and ((control\$4 or arbit\$6 or schedul\$4) near8 random\$2 near8 (writ\$3 or stor\$3 or select\$3 or priorit\$6))	595	<u>L4</u>
<u>L3</u>	L1 and ((control\$ or arbit\$6 or schedul\$4) near8 random\$2 near8 (writ\$3 or stor\$3 or select\$3 or priorit\$6))	596	<u>L3</u>
<u>L2</u>	L1 and ((control\$ or arbit\$6 or schedul\$4) near8 random\$2 near8 (writ\$3 or stor\$3 or select\$3))	581	<u>L2</u>
<u>L1</u>	(DRAM or SRAM or bank) near4 (control\$4 or arbit\$6 or schedul\$4)	14518	<u>L1</u>

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<u>L11</u>	L10 and ((DRAM or (dynamic near4 memory)) same (gygabyte or gbps))	1	<u>L11</u>
<u>L10</u>	((711/105)!.CCLS.)	438	<u>L10</u>
<u>L9</u>	(DRAM or (dynamic near4 memory)) same gygabyte	0	<u>L9</u>
<u>L8</u>	(DRAM or (dynamic near4 memory)) same 5Gps	0	<u>L8</u>
<u>L7</u>	(latency or speed or delay or bandwidth) same (memory or bank or module or DRAM or cell or row) same (binomial or Bernoulli)	39	<u>L7</u>
<u>L6</u>	(latency or speed or delay or bandwidth) same (memory or bank or module or DRAM) same binomial	9	<u>L6</u>
<u>L5</u>	l3 and (module or bank or cell)	52	<u>L5</u>
<u>L4</u>	L3 and DRAM	0	<u>L4</u>
<u>L3</u>	(latency or speed or delay or bandwidth) same binomial	100	<u>L3</u>
<u>L2</u>	L1 and binomial	0	<u>L2</u>
<u>L1</u>	(((((((packet near6 (stor\$4 or writ\$4 or buffer\$3 or queu\$3 or receiv\$4))and ((choos\$3 or chos\$4 or pick\$4 or writ\$3 or stor\$3 or select\$3 or priorit\$6 or order\$3 or arrang\$3) near6 (random\$2 or arbitrar\$3 or different) near8 (module or bank or location or cell or memories)))and ((control\$4 or arbit\$6 or schedul\$4) near8 (memory or memories or bank or module or location or cell or queu\$3 or buffer\$3)))and ((random\$2 or arbitrar\$3) same (speed or latency or bandwidth)))and ((memory or memories or bank or module or location or cell) with parallel))and ((711/\$ or 709/\$ or 370/\$ or 710/\$ or 365/\$.ccls.)) and ((binomial or dual\$4) near8 (bank or module or cell or memory)))	54	<u>L1</u>

END OF SEARCH HISTORY